

(11) **EP 1 061 662 A1**

(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 158(3) EPC

(43) Date of publication:
20.12.2000 Bulletin 2000/51

(51) Int. Cl.⁷: **H04B 1/50**

(21) Application number: **99954418.2**

(86) International application number:
PCT/JP99/06191

(22) Date of filing: **08.11.1999**

(87) International publication number:
WO 00/28673 (18.05.2000 Gazette 2000/20)

(84) Designated Contracting States:
**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE**

- **YAHATA, Kazuhiro**
Nishikasugai-gun, Aichi 452-0946 (JP)
- **TSUNEOKA, Michiaki**
Ibaraki-shi, Osaka 567-0887 (JP)

(30) Priority: **10.11.1998 JP 31873698**

(71) Applicant:
MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.
Kadoma-shi, Osaka-fu, 571 (JP)

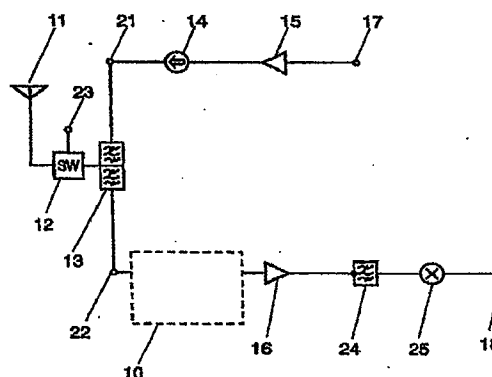
(74) Representative:
Kügele, Bernhard et al
NOVAPAT INTERNATIONAL SA,
9, Rue du Valais
1202 Genève (CH)

(72) Inventors:
• **SAKAI, Yukio**
Kawanishi-shi, Hyogo 666-0015 (JP)

(54) **HIGH-FREQUENCY RADIO CIRCUIT**

(57) The invention relates to a high frequency wireless circuit apparatus mainly used in mobile communication appliances such as a portable telephone and an information communication terminal. It is hence an object to improve the reception sensitivity and immunity to interference signals. The high frequency wireless circuit apparatus of the invention is composed by connecting a phase shifter 10 between an antenna duplexer 13 and a low noise amplifier 16, so that the impedance of the receiving terminal of the antenna duplexer 13 at a transmission frequency and the input impedance of the low noise amplifier 16 may not be matched in complex conjugates of each other. Therefore it prevents the transmission output passing through the receiving terminal of the antenna duplexer 13 and the interference signal entered through the antenna 11 from making the cross modulation in the low noise amplifier. Thus it improves the reception sensitivity and immunity to interference signals of the high frequency wireless circuit apparatus at the same time.

FIG. 1



EP 1 061 662 A1

Description

TECHNICAL FIELD

[0001] The present invention relates to a high frequency wireless circuit apparatus mainly used in mobile communication appliances such as a portable telephone and an information communication terminal.

BACKGROUND ART

[0002] Generally, the high frequency wireless circuit apparatus of this type has been composed as shown in Fig. 6. In the drawing, the reference numeral 11 denotes an antenna; 12 denotes a switch; 13 denotes an antenna duplexer; 14 denotes an isolator; 15 denotes a power amplifier; 16 denotes a low noise amplifier; 17 denotes a transmitting input terminal; and 18 denotes a receiving output terminal. The antenna duplexer 13 and the low noise amplifier 16 have been connected directly without any interposing means. Reference numerals 21, 22 are respectively a transmitting terminal and a receiving terminal of the antenna duplexer 13.

[0003] The transmission signal input from the transmitting input terminal 17 is amplified in the power amplifier 15, and is fed into the transmitting terminal 21 of the antenna duplexer 13 through the isolator 14. The isolator 14 is provided so that the transmission characteristic of the power amplifier 15 may not fluctuate even if the load of the antenna 11 changes. The transmission signal output from the antenna duplexer 13 is radiated into the air from the antenna 11 through the switch 12.

[0004] On the other hand, the reception signal input from the antenna 11 is fed into the antenna duplexer 13 through the switch 12, and is output into the receiving terminal 22 of the antenna duplexer 13. This reception signal is input to the low noise amplifier 16 through a phase shifter 10, and amplified, and sent out to the receiving output terminal 18.

[0005] The transmission signal fed into the antenna duplexer 13 is also sent to the receiving terminal 22 of the antenna duplexer 13. The switch 12 is not always necessary, or two or more switches may be connected to the contrary.

[0006] In such conventional structure, however, when the antenna duplexer 13 and the low noise amplifier 16 are directly connected, the transmission power amplified in the power amplifier 15 passes through the receiving terminal 22 of the antenna duplexer 13 and is mixed into the low noise amplifier 16. The transmission power amplified possibly causes cross modulation with an incoming interference signal from the antenna 11. As a result the reception sensitivity deteriorates.

[0007] In particular, this phenomenon occurs frequently in the CDMA system wireless communication, and it has been difficult to satisfy the interference wave input level of -30 dBm in the single wave interference characteristic determined in the standard.

[0008] That is, in the case of CDMA system wireless communication, the transmission signal output to the reception signal terminal 22 of the antenna duplexer 13 is modulated by CDMA, and has a modulation band width. On the other hand, the reception signal input through the antenna 11 is a CDMA modulated wave same as the transmission signal, but the interference signal input through the antenna 11 is a non-modulated signal. When the non-modulated interference wave and the CDMA modulated transmission signal are fed into the low noise amplifier 16, the two-wave input distortion occurs. Then the intermodulation distortion and cross modulation distortion occurs. The CDMA modulated wave has a band. Then the wave caused by the cross modulation of interference waves and the transmission wave covers the reception signal band, thereby disabling the reception.

DISCLOSURE OF THE INVENTION

[0009] It is hence an object of the invention to enhance the reception sensitivity and immunity to interference signals in a high frequency wireless circuit apparatus used in mobile communication appliances such as a portable telephone and an information communication terminal.

[0010] To achieve the object, the high frequency wireless circuit apparatus of the invention is composed by connecting a phase shifter between an antenna duplexer and a low noise amplifier, so that the impedance of the receiving terminal of the antenna duplexer at a transmission frequency and the input impedance of the low noise amplifier may not be matched in complex conjugates of each other. It prevents the transmission output passing through the receiving terminal of the antenna duplexer and the interference signal entered through the antenna from causing the cross modulation in the low noise amplifier. Thus the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus improve at the same time.

BRIEF DESCRIPTION OF THE DRAWINGS

[0011]

Fig. 1 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 1 of the invention.

Fig. 2 is an electric characteristic diagram of the high frequency wireless circuit apparatus in embodiment 1 of the invention.

Fig. 3 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 2 of the invention.

Fig. 4 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 3 of the invention.

Fig. 5 is a mounting layout showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 4 of the invention.

Fig. 6 is an electric circuit diagram showing a conventional high frequency wireless circuit apparatus.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0012] In the high frequency wireless circuit apparatus of the invention, a switch is connected between an antenna and an antenna terminal of an antenna duplexer, an isolator is connected between a transmitting terminal of the antenna duplexer and an output terminal of a power amplifier, and a phase shifter is provided between a receiving terminal of the antenna duplexer and an input terminal of a low noise amplifier. Thus the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus can be enhanced simultaneously.

[0013] Preferably, an impedance of the receiving terminal of the antenna duplexer at the transmission frequency is shifted by the phase shifter by at least ± 45 degrees or more from the point at which the impedance matches with an input impedance of the low noise amplifier in complex conjugates of each other. By the constitution in which the impedance of the receiving terminal of the antenna duplexer at the transmission frequency and the input impedance of the low noise amplifier may not be matched in complex conjugates of each other. Thus the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus can be enhanced effectively.

[0014] Preferably, the phase shifter is composed of a transmission line, and the freedom of a connection between the receiving terminal of antenna duplexer and the input terminal of low noise amplifier can be enhanced.

[0015] Preferably, the phase shifter is composed of a band pass filter, so that the high frequency wireless circuit apparatus can be further reduced in size.

[0016] Preferably, the switch, antenna duplexer, isolator and power amplifier are formed at one side of a double-sided multi-layer substrate, and the phase shifter and low noise amplifier are formed at the other side of the double-sided multi-layer substrate. Thus a direct coupling of transmission signal with the low noise amplifier is prevented, so that the reception sensitivity and immunity to interference signals of the high frequency wireless circuit apparatus can be enhanced at the same time.

(Embodiment 1)

[0017] Fig. 1 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 1 of the invention. In the case of CDMA system wireless communication, as

the antenna input and output section, generally, the high frequency wireless circuit apparatus as shown below is used.

[0018] That is, a switch 12 connected between an antenna 11 and an antenna duplexer 13 is provided for testing the high frequency wireless circuit apparatus from outside, and the switch 12 may be composed of either a mechanical type for changing over the signal path or a semiconductor element type. When testing from outside, the antenna duplexer 13 is electrically disconnected from the antenna 11, and is connected to an external terminal 23.

[0019] The antenna duplexer is provided for separating the transmission frequency and reception frequency, and in particular a sufficient separation characteristic is established so that the transmission output may not leak to the reception side. In the embodiment, this separation characteristic is 56 dB or more so that the reception sensitivity may not deteriorate. The antenna duplexer 13 may be composed of either dielectric element or SAW.

[0020] A low noise amplifier 16 is provided for lowering the noise level at the reception side, and it enhances the reception sensitivity of the system. In the embodiment, the forward power gain of the low noise amplifier 16 is 18 dB or more, the noise figure is 1.5 dB or less, and the equivalent input third-order intermodulation distortion characteristic -1 dBm or more.

[0021] An isolator 14 is connected between a power amplifier 15 and the antenna duplexer 13. The isolator 14 transmits the transmission signal only in one direction of the antenna duplexer 13 from the power amplifier 15. Therefore, if the load of the power amplifier 15 is changed depending on the antenna 11, the output side of the power amplifier 15 is free from the effects, thereby preventing the deterioration of adjacent-channel leakage power characteristic of the power amplifier 15. As shown in Fig. 1, a phase shifter 10 is used in the embodiment.

[0022] The phase shifter 10 is constituted so that the impedance of a receiving terminal 22 of the antenna duplexer 13 at transmission frequency and the input impedance of the low noise amplifier 16 may not be matched in complex conjugates of each other. Hence, the transmission signal passing through the receiving terminal 22 of the antenna duplexer 13 and the interference signal entered from the antenna 11 do not cause cross modulation in the low noise amplifier 16. Since the impedance of the receiving terminal 22 of the antenna duplexer 13 at the transmission frequency and the input impedance of the low noise amplifier 16 are not matched at complex conjugates of each other, the transmission signal power entered in the low noise amplifier 16 may be minimum. Thus the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus may be enhanced.

[0023] Incidentally, the phase shifter 10 is not particularly required to shift the reception frequency. If the

5

EP 1 061 662 A1

6

reception frequency is shifted, the desired reception signal may be mismatched with the input impedance of the low noise amplifier 16, and it is hard to feed into the low noise amplifier 16. This embodiment is particularly effective in the CDMA system wireless communication in which the transmission frequency and reception frequency are close to each other, but it is sufficiently effective also in the conventional FDMA or TDMA system wireless communication in which the transmission frequency and reception frequency are remote from each other.

[0024] A filter 24 is connected in a later stage of the low noise amplifier 16, and the filter 24 is followed by a reception mixer 25. The input impedance of this filter is designed to be matched at the reception frequency and mismatched at the transmission frequency. Therefore the input impedance of the low noise amplifier 16 has an effect of the input impedance of the filter 24. Therefore, shifting of the transmission frequency by the phase shifter 10 is effective.

[0025] Fig. 2 is an electric characteristic diagram of the high frequency wireless circuit apparatus in embodiment 1 of the invention. In Fig. 2, the axis of abscissas represents the shifting extent of the impedance of transmission frequency by the phase shifter 10 at the receiving terminal 22 of the antenna duplexer 13, and the axis of ordinates denotes the level of interference signal entered through the antenna 11. As shown in Fig. 2, when the shifting extent of the impedance at the transmission frequency at the receiving terminal 22 of the antenna duplexer 13 by the phase shifter 10 is more than ± 45 degrees, a signal can be entered from the antenna 11 to a certain interference signal level. However, when the shifting extent is smaller than ± 45 degrees, the allowable values of interference signal levels deteriorate extremely.

[0026] In the embodiment, the shifting extent of impedance at the transmission frequency at the receiving terminal 22 of the antenna duplexer 13 by the phase shifter 10 is set at 70 to 110 degrees so as to be realized easily in an actual circuit, but the non-deteriorating range of characteristic may be defined by the shifting extent ranging from -180 to -45 degrees, or 45 to 180 degrees. Hence it is possible to alleviate the equivalent input third-order intermodulation distortion characteristic of the low noise amplifier 16. Or, the input level of interference wave can be increased.

(Embodiment 2)

[0027] Fig. 3 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 2 of the invention. As shown in Fig. 3, the phase shifter 10 is composed of a transmission line 10a. A microstrip line, a strip line, a coaxial line, or any other system may realize the transmission line 10a. In the embodiment, in particular, microstrip lines or strip lines connect the receiving ter-

minal 22 of the antenna duplexer 13 and the low noise amplifier 16. This is because the transmission loss is reduced between the receiving terminal 22 of the antenna duplexer 13 and the low noise amplifier 16, and the connection is easy.

[0028] In the embodiment, the connection between the receiving terminal 22 of the antenna duplexer 13 and the low noise amplifier 16 is realized from one side of the double-sided substrate to the other side of the double-sided substrate. When this double-sided substrate is composed in a multi-layer substrate, it is effective for the inner layer grounding to prevent high-frequency coupling of the transmission signal with the low noise amplifier 16.

(Embodiment 3)

[0029] Fig. 4 is an electric circuit diagram showing a schematic configuration of a high frequency wireless circuit apparatus in embodiment 3 of the invention. As shown in Fig. 4, the phase shifter 10 is composed of a band pass filter. Herein, the state composed by the transmission line 10a is realized with lumped element circuits, and it is composed of capacitors and a coil.

[0030] In this composition, by adjusting the capacitor or coil, the shifting extent of the phase shifter 10 can be freely changed, so that the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus can be optimally enhanced.

[0031] In Fig. 4, in order to minimize the effects of second harmonics and third harmonics of the transmission signal, the band pass filter for composing the phase shifter 10 is of low pass type. However, a high pass type may also realize the phase shifter 10.

(Embodiment 4)

[0032] Fig. 5 is a mounting layout of a high frequency wireless circuit apparatus in embodiment 4 of the invention. As shown in Fig. 5, the switch 12, antenna duplexer 13, isolator 14 and power amplifier 15 are formed at one side of a double-sided substrate 20, and the phase shifter 10 and low noise amplifier 16 are formed at other side of the double-sided substrate 20. In this state, an intermediate layer 30 of the double-sided substrate 20 is a grounding pattern.

[0033] In this constitution, therefore, by the grounding pattern of the intermediate layer 30 of the double-sided substrate 20, direct mixing of transmission output from the power amplifier 15 into the low noise amplifier 16 is shielded. As the result, the reception sensitivity and immunity to interference signal of the high frequency wireless circuit apparatus can be enhanced at the same time.

[0034] Incidentally, the phase shifter 10 may be disposed at one side only of the double-sided substrate 20. In this constitution, the transmission unit and reception unit can be separated by the double-sided substrate 20.

7

EP 1 061 662 A1

8

INDUSTRIAL APPLICABILITY

[0035] According to the invention, as described herein, a switch is connected between an antenna and an antenna terminal of an antenna duplexer, an isolator is connected between a transmitting terminal of the antenna duplexer and an output terminal of a power amplifier, and a phase shifter is provided between a receiving terminal of the antenna duplexer and an input terminal of a low noise amplifier. Therefore the reception sensitivity and immunity to interference signals of the high frequency wireless circuit apparatus can be enhanced simultaneously. It also solves the problems of deterioration of reception sensitivity due to cross modulations caused by the interference signal entered through the antenna and the transmission power amplified by the power amplifier, which passes through the receiving terminal of the antenna duplexer and mixes into the low noise amplifier.

List of Reference Symbols

[0036]

- 11 Antenna
- 12 Switch
- 13 Antenna duplexer
- 14 Isolator
- 15 Power amplifier
- 16 Low noise amplifier
- 17 Transmitting input terminal
- 18 Receiving output terminal
- 20 Double sided substrate
- 21 Transmitting terminal
- 22 Receiving terminal
- 23 External terminal
- 24 Filter
- 25 Receiving mixer
- 30 Intermediate layer

Claims

1. A high frequency wireless circuit apparatus comprising:

a switch connected to between an antenna and an antenna terminal of an antenna duplexer; an isolator connected between a transmitting terminal of the antenna duplexer and an output terminal of a power amplifier; and a phase shifter disposed between a receiving terminal of said antenna duplexer and an input terminal of a low noise amplifier.

2. The high frequency wireless circuit apparatus of claim 1, wherein an impedance of the receiving terminal of the antenna duplexer at a transmission frequency is shifted by at least ± 45 degrees or more

from the point at which the impedance matches with an input impedance of the low noise amplifier in complex conjugates of each other by means of said phase shifter.

3. The high frequency wireless circuit apparatus of claim 1, wherein said phase shifter is composed of a transmission line.
4. The high frequency wireless circuit apparatus of claim 1, wherein said phase shifter is composed of a band pass filter.
5. The high frequency wireless circuit apparatus of claim 1, wherein said switch, the antenna duplexer, said isolator and the power amplifier are formed on one side of a double-sided multi-layer substrate, and said phase shifter and a low noise amplifier are formed at the other side of said double-sided multi-layer substrate.

Amended claims under Art. 19.1 PCT

1. (Amended) A high frequency wireless circuit apparatus for transmitting and receiving thorough an antenna duplexer comprising:

an antenna connected to an antenna terminal of said antenna duplexer; an isolator connected between a transmitting terminal of said antenna duplexer and an output terminal of a power amplifier; and a phase shifter disposed between a receiving terminal of said antenna duplexer and an input terminal of a low noise amplifier.

2. (Amended) The high frequency wireless circuit apparatus of claim 1, wherein said phase shifter shifts a impedance of the receiving terminal of said antenna duplexer at a transmission frequency by at least ± 45 degrees or more from a point at which the impedance matches with an input impedance of the low noise amplifier in complex conjugates of each other.

3. The high frequency wireless circuit apparatus of claim 1, wherein said phase shifter is composed of a transmission line.

4. The high frequency wireless circuit apparatus of claim 1, wherein said phase shifter is composed of a band pass filter.

5. The high frequency wireless circuit apparatus of claim 1, wherein a switch, said antenna duplexer, said isolator and the power amplifier are formed on one side of a double-sided multi-layer substrate, and said phase shifter and the low noise amplifier

9

EP 1 061 662 A1

10

are formed at the other side of said double-sided
multi-layer substrate.

5

10

15

20

25

30

35

40

45

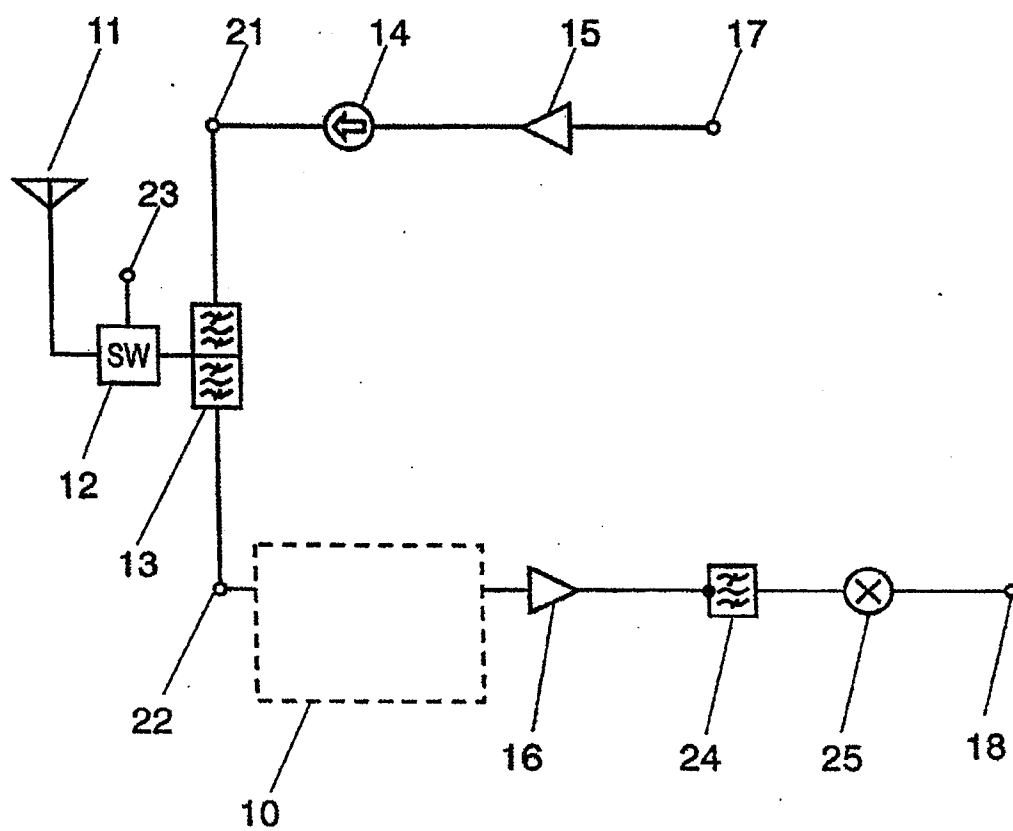
50

55

6

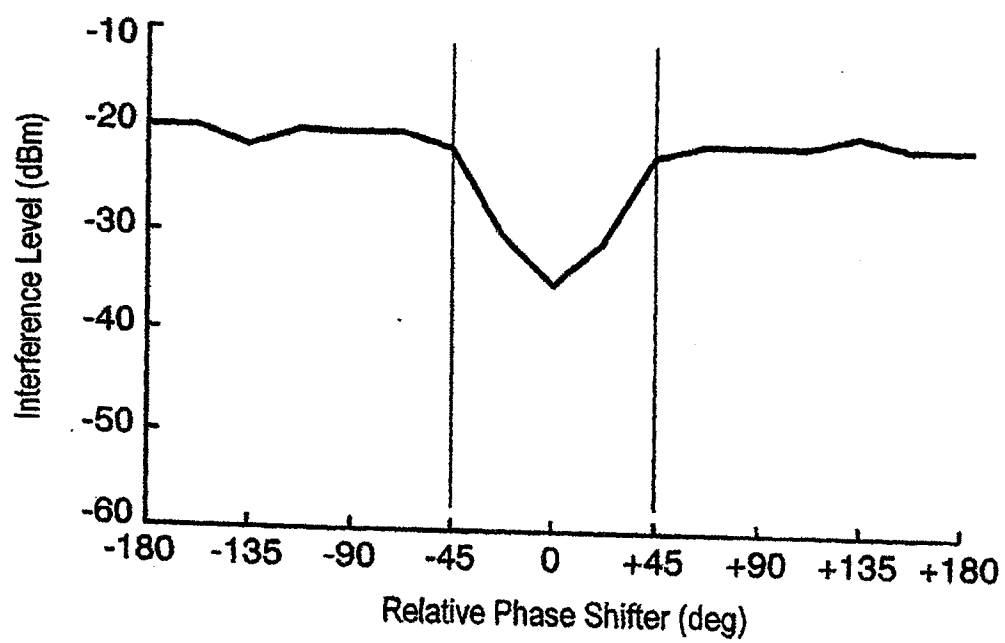
EP 1 061 662 A1

FIG. 1



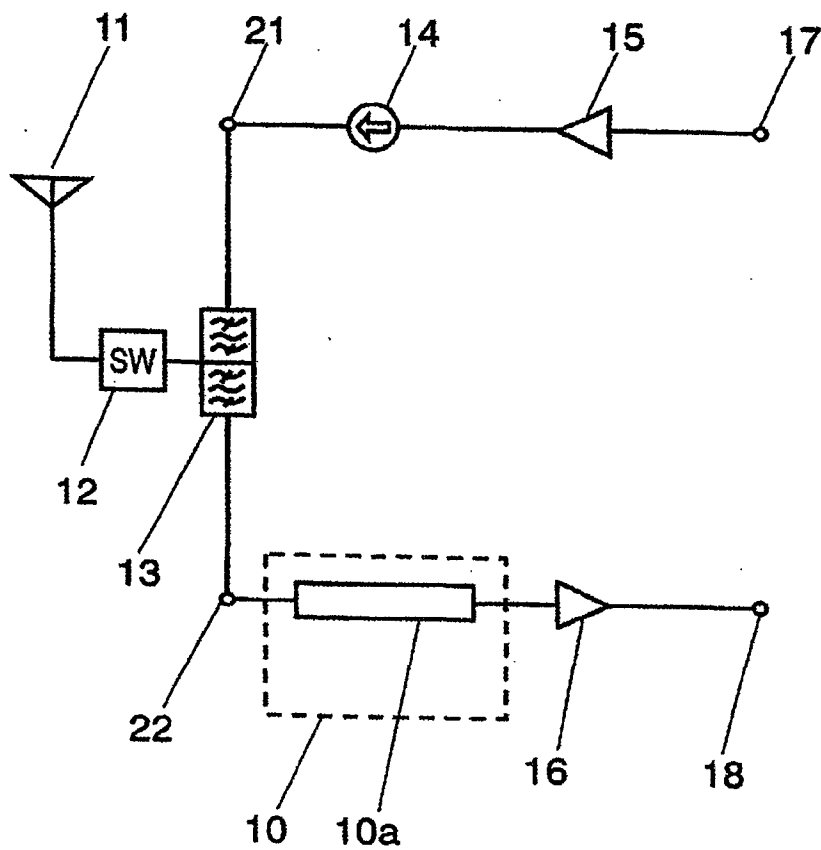
EP 1 061 662 A1

FIG. 2



EP 1 061 662 A1

FIG. 3



EP 1 061 662 A1

FIG. 4

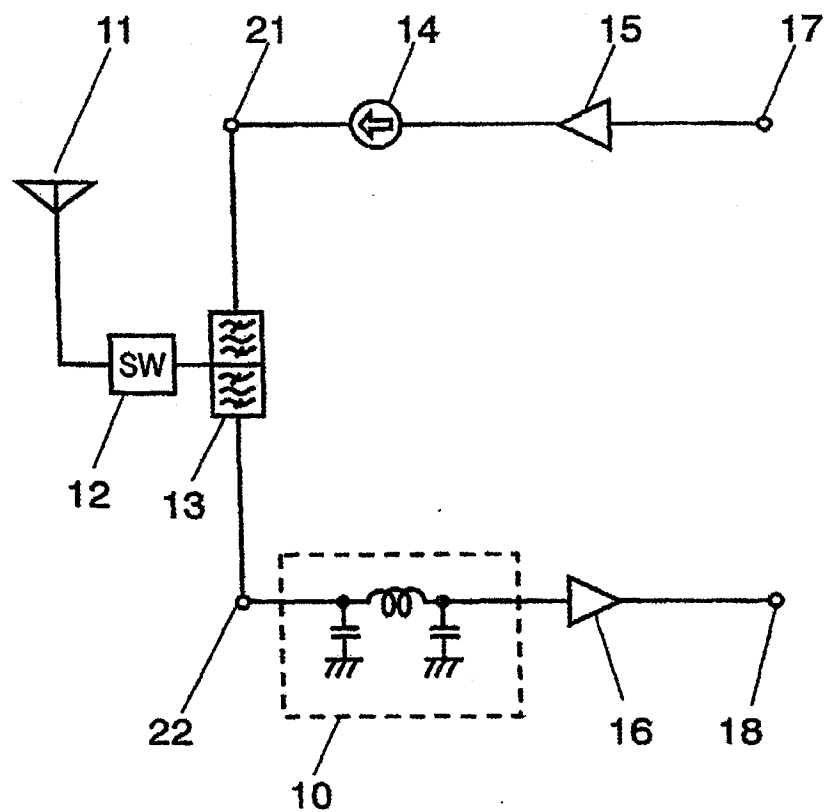
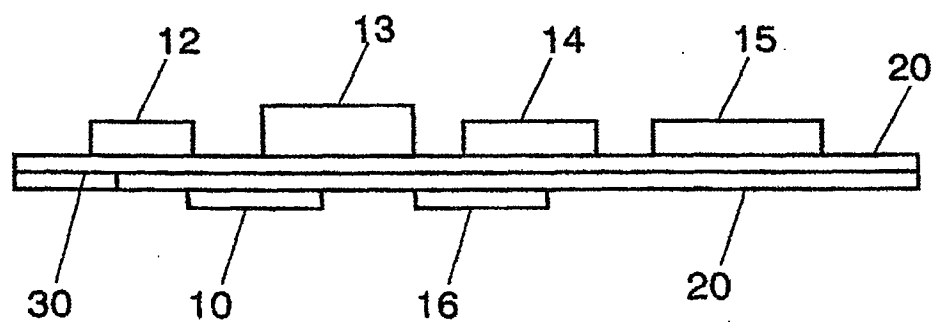
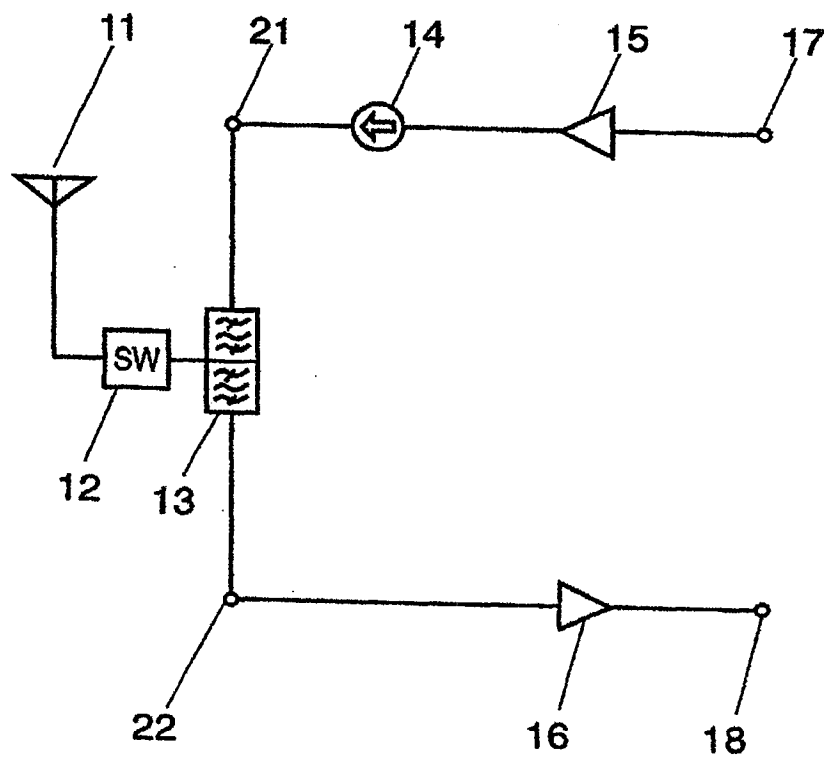


FIG. 5



EP 1 061 662 A1

FIG. 6



EP 1 061 662 A1

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/06191

A. CLASSIFICATION OF SUBJECT MATTER
Int.Cl⁷ H04B1/50

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
Int.Cl⁷ H04B1/04 , H04B1/38 , H04B1/50 , H03F3/60

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Jitsuyo Shinan Koho 1922-2000 Toroku Jitsuyo Shinan Koho 1994-2000
Kokai Jitsuyo Shinan Koho 1971-2000 Jitsuyo Shinan Toroku Koho 1996-2000

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US, 4692726, A (Motorola, Inc.), 08 September, 1987 (08.09.87), column 5, lines 31-33; column 6, lines 18-29; Figs. 8, 10 & DK, 64488, A & WO, 88/01104, A & NO, 881269, A & CN, 87105317, A & FI, 890243, A & US, 4829274, A & EP, 318478, A & US, 4954796, A & CA, 1277729, A & CN, 1048951, A & CA, 1287667, A & AT, 118653, E & DE, 3751062, C & KR, 9503103, B & JP, 01-503428, A page 6, lower left column, lines 15-18; page 7, upper left column, lines 14-23; Figs. 8, 10	1, 3
Y	JP, 3-101523, A (Fujitsu Limited), 26 April, 1991 (26.04.91), column "Jurai no Gijutsu" (Family: none)	1, 3
Y	JP, 6-7332, U (Toshiba Corporation), 28 January, 1994 (28.01.94), Fig. 5 (Family: none)	1, 3

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

* Special categories of cited documents:
"A" document defining the general state of the art which is not
considered to be of particular relevance
"E" earlier document but published on or after the international filing
date
"L" document which may throw doubts on priority claim(s) or which is
cited to establish the publication date of another citation or other
special reason (as specified)
"O" document referring to an oral disclosure, use, exhibition or other
means
"P" document published prior to the international filing date but later
than the priority date claimed

"T" later document published after the international filing date or
priority date and not in conflict with the application but cited to
understand the principle or theory underlying the invention
"X" document of particular relevance; the claimed invention cannot be
considered novel or cannot be considered to involve an inventive
step when the document is taken alone
"Y" document of particular relevance; the claimed invention cannot be
considered to involve an inventive step when the document is
combined with one or more other such documents, such
combination being obvious to a person skilled in the art
"&" document member of the same patent family

Date of the actual completion of the international search
24 January, 2000 (24.01.00)

Date of mailing of the international search report
08 February, 2000 (08.02.00)

Name and mailing address of the ISA/
Japanese Patent Office

Authorized officer

Facsimile No.

Telephone No.

Form PCT/ISA/210 (second sheet) (July 1992)